

Applicant: IWANISHI, et al.
Docket: 10873.1199US01
Title: METHOD FOR DESIGNING SEMICONDUCTOR INTEGRATED CIRCUIT

By: [Signature]
Name: John Jenkins

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

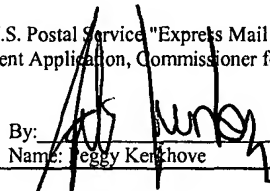
Applicant:	Iwanishi, et al.	Examiner:	Unknown
Serial No.:	Unknown	Group Art Unit:	Unknown
Filed:	Herewith	Docket No.:	10873.1199US01
Title:	METHOD FOR DESIGNING SEMICONDUCTOR INTEGRATED CIRCUIT		

CERTIFICATE UNDER 37 CFR 1.10:

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I hereby certify that this paper or fee is being deposited with the U.S. Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

By: 
Name: Peggy Kerkhove

COMMUNICATION REGARDING REQUESTED FIGURE

Mail Stop New App
Assistant Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Dear Sir:

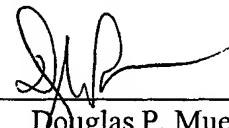
In connection with the above-identified application, applicant respectfully requests that Figure 1 be printed on the front page of the published application.

If a telephone conference would be helpful in resolving any issues concerning this communication, please contact Applicants' primary attorney-of record Douglas P. Mueller (Reg. No. 30,300), at (612) 371.5237.

Respectfully submitted,

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Dated: June 20, 2003

By: 
Douglas P. Mueller
Reg. No. 30,300

DPM/pjk

